

Listing of Claims

1. (Previously Presented) An integrated circuit provided with at least one output driver without contacts to a substrate and further provided with at least a core region with a Vssc contact and a periphery region provided with at least one Vssq contact, characterized in that a resistance with a value lying between 100 and 300 ohms is provided between each Vssq contact and the Vssc contact.
2. (Previously Presented) An integrated circuit as claimed in claim 1, wherein the at least one output driver is not slew-rate controlled, characterized in that the value of the resistance is greater than 250 ohms.
3. (Previously Presented) An integrated circuit as claimed in claim 1, wherein the at least one output driver is slew-rate controlled, characterized in that the value of the resistance is at most 250 ohms.
4. (Cancelled).

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